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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Satoh

Serial No.: 09/981,402

Filed: October 17, 2001

Title: Semiconductor Device and Method for
Its Manufacture

Attorney Docket No.: N32040200W



Group Art Unit: 2812
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Examiner: Tsai, H Jey

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RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The following is submitted in response to the Office Action dated May 13, 2002, and is currently due June 13, 2002.

REMARKS

Provisional Election with Traverse

In response to the restriction requirement set forth in the Office Action dated May 7, 2002, the claims of Group 1 (claims 1-6) are provisionally elected with traverse.

37 C.F.R. §1.8

I hereby certify that this correspondence is being

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deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: June 10, 2002 _____

Typed/Printed Name: Darryl Walker _____

Signature: Darryl Walker _____